CS309 Digital Logic Lab Exercise 1
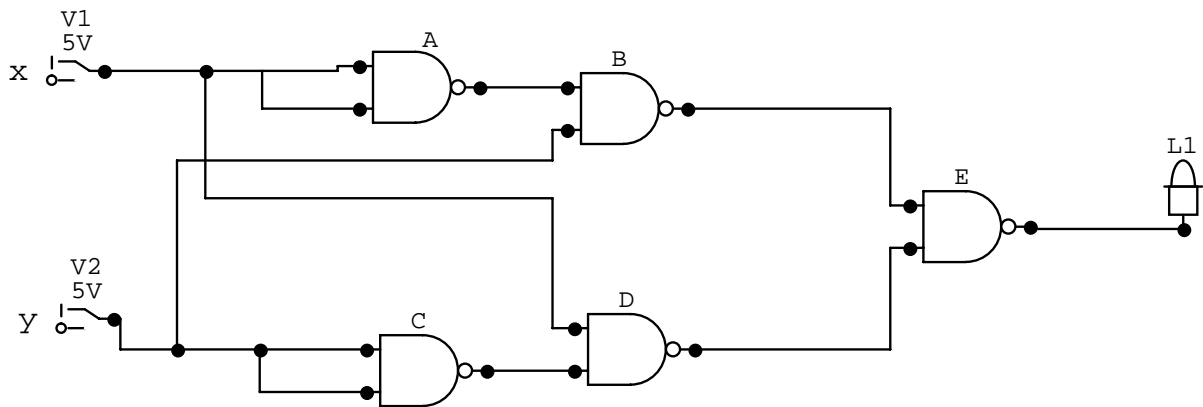
You are to implement a 2-level NAND realization of the 2-input Exclusive OR function (x XOR y = x'y + xy').

For this exercise, your instructor will give you the design. For all future labs, you will do the design yourself.

Logic Design

Wiring Diagram